

Review of Performance: VEE 103 Electronics Fundamentals I Fall 2016, (17 students) P1
Submitted by: Danilo S. Ibarrola

Institutional Student Learning Outcomes (ISLO):

- ILO1:** Effective oral communication.
- ILO2:** Effective written communication.
- ILO3:** Critical Thinking
- ILO4:** Problem Solving
- ILO5:** Inter-cultural knowledge and competence.
- ILO6:** Information literacy.
- ILO7:** Foundations and skills for life-long learning.
- ILO8:** Quantitative reasoning.

Program Learning Outcomes (PLO)

- PLO1:** Practice Safety and occupational health procedures in the workplace.
- PLO2:** Use electronic tools and test equipment competently.
- PLO3:** Interpret schematic diagrams and waveforms.
- PLO4:** Build electronic projects to a given specification.

SLO#	Program SLO#	I, D, M	ISLO	Reflection/Comment	
1. Describe the fundamentals of voltage and current and the behavior of these parameters in simple electrical circuits.	3. Interpret schematic diagrams and waveforms.	I, D	6, 7	Course Result	SLO was assessed by written test questions using the assessment criteria as stated in the course outline. 12 (11 male & 1 female) out of 17 students (70.6%) completed the CSLO.
				Target Met	Yes
				Students need more time in hands-on and other practical procedure to reach mastery level performance.	

2. Explain the purpose and identify the various types of resistors and their symbols. Identify the value, power rating and tolerance of resistors using various types of industry codes.	3. Interpret schematic diagrams and waveforms.	I,D	6, 7	Course Result	SLO was assessed by written test questions using the assessment criteria as stated in the course outline. 15 (14 male & 1 female) out of 17 students (88.2%) completed the CSLO.
				Target Met	Yes
Students need more time in hands-on and other practical procedure to reach mastery level performance.					
3. Describe the purpose and types of switches, fuses and circuit breakers and identify their schematic symbols.	3. Interpret schematic diagrams and waveforms.	I, D	6, 7	Course Result	SLO was assessed by written test questions using the assessment criteria as stated in the course outline. 13 (13 male & 0 female) out of 17 students (76.5%) completed the CSLO.
				Target Met	Yes
Students need more time in hands-on and other practical procedure to reach mastery level performance.					
4. Define magnetism and electromagnetism and their characteristics; describe how these characteristics are utilized in the operation of the relay, magnetic circuit breaker and meter.	3. Interpret schematic diagrams and waveforms.	I, D	6, 7	Course Result	SLO was assessed by written test questions using the assessment criteria as stated in the course outline. 16 (15 male & 1 female) out of 17 students (94.1%) completed the CSLO.
				Target Met	Yes
Students need more time in hands-on and other practical procedure to reach mastery level performance.					
5. Describe the function of the multimeter and its controls. Safely and accurately use a	1.Practice safety and occupational health	I, D, M	6, 7	Course Result	SLO was assessed by written test questions using the assessment criteria as stated in the course outline. 16 (15 male & 1 female) out of 17 students (94.1%) completed the CSLO.

<p>multimeter to measure the circuit quantities of resistance, voltage, and current.</p>	<p>procedures in the workplace. 2. Use electronics tools and test equipment competently. 3. Interpret schematic diagrams and waveforms.</p>			<table border="1"> <tr> <td data-bbox="976 246 1241 277">Target Met</td> <td data-bbox="1241 246 1915 277">Yes</td> </tr> <tr> <td colspan="2" data-bbox="976 318 1915 623">Students need more time in hands-on and other practical procedure to reach mastery level performance.</td> </tr> </table>		Target Met	Yes	Students need more time in hands-on and other practical procedure to reach mastery level performance.			
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<p>6. Using Ohm's Law to define the relationship between resistance, voltage, current, and power in an electrical circuit. By experimentation prove Ohm's Law.</p>	<p>3. Interpret schematic diagrams and waveforms.</p>	<p>I, D</p>	<p>4, 6, 7</p>	<table border="1"> <tr> <td data-bbox="976 636 1241 808">Course Result</td> <td data-bbox="1241 636 1915 808">SLO was assessed by written test questions using the assessment criteria as stated in the course outline. 12 (11 male & 1 female) out of 17 students (70.6%) completed the CSLO.</td> </tr> <tr> <td data-bbox="976 816 1241 847">Target Met</td> <td data-bbox="1241 816 1915 847">Yes</td> </tr> <tr> <td colspan="2" data-bbox="976 888 1915 985">Students need more time in hands-on and other practical procedure to reach mastery level performance.</td> </tr> </table>		Course Result	SLO was assessed by written test questions using the assessment criteria as stated in the course outline. 12 (11 male & 1 female) out of 17 students (70.6%) completed the CSLO.	Target Met	Yes	Students need more time in hands-on and other practical procedure to reach mastery level performance.	
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<p>7. Identify the following circuits, calculate and measure the circuit parameters of voltage, resistance, and current. Troubleshoot the series, parallel and series-parallel circuits. a. Series Circuit b. Parallel Circuit</p>	<p>3. Interpret schematic diagrams and waveforms.</p>	<p>I, D</p>	<p>4, 6, 7</p>	<table border="1"> <tr> <td data-bbox="976 998 1241 1170">Course Result</td> <td data-bbox="1241 998 1915 1170">SLO was assessed by written test questions using the assessment criteria as stated in the course outline. 14 (13 male & 1 female) out of 17 students (82.3%) completed the CSLO.</td> </tr> <tr> <td data-bbox="976 1179 1241 1209">Target Met</td> <td data-bbox="1241 1179 1915 1209">Yes</td> </tr> <tr> <td colspan="2" data-bbox="976 1250 1915 1331">Students need more time in hands-on and other practical procedure to reach mastery level performance.</td> </tr> </table>		Course Result	SLO was assessed by written test questions using the assessment criteria as stated in the course outline. 14 (13 male & 1 female) out of 17 students (82.3%) completed the CSLO.	Target Met	Yes	Students need more time in hands-on and other practical procedure to reach mastery level performance.	
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c. Series and Parallel Circuit d. Voltage Divider Circuit e. Bridge Circuit					
8. Simplify and analyze complex circuits using the following methods: a. Kirchhoff's Laws b. Thevenin's Theorem c. Norton's Theorem	3. Interpret schematic diagrams and waveforms.	I, D	6,7	Course Result	SLO was assessed by written test questions using the assessment criteria as stated in the course outline. 13 (12 male & 1 female) out of 17 students (76.5%) completed the CSLO.
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Special comments: 15 out of 17 or 88.2% of the students got a grade of C and higher and 2 or 11.8% got D & F.

Summary of Grades:

A+ = 0
A = 3
A- = 2
B+ = 4
B = 5
B- = 1
C+ = 0
C = 0
C- = 0
D+ = 0
D = 0

D- = **1**
F = **1**

Recommendations: Laboratory equipment (NIDA cards) for Electronics Fundamentals I must be enough for at least 3 to 5 sets to be able for the students to perform their required experimentation. Additional quality analog and digital multi-meter must also be purchase so that more hands on experimentation can be done.

Signature: **DANILO S. IBARROLA**
Instructor

Date: DEC. 2016