

**Review of Performance:** VEE 222 Discrete Devices 2, Fall 2016, 13 students)  
**Submitted by:** Danilo S. Ibarrola

**Institutional Student Learning Outcomes (ISLO):**

- ILO1:** Effective oral communication.
- ILO2:** Effective written communication.
- ILO3:** Critical Thinking
- ILO4:** Problem Solving
- ILO5:** Intercultural knowledge and competence.
- ILO6:** Information literacy.
- ILO7:** Foundations and skills for life-long learning.
- ILO8:** Quantitative reasoning.

**Program Learning Outcomes (PLO)**

- PLO1:** Practice Safety and occupational health procedures in the workplace.
- PLO2:** Use electronic tools and test equipment competently.
- PLO3:** Interpret schematic diagrams and waveforms.
- PLO4:** Build electronic projects to a given specification.

SLO#	Program SLO#	I, D, M	ISLO	Reflection/Comment	
1. Describe the purpose and operation of Unijunction Transistor (UJT) and SCR.	3. Interpret schematic diagrams and waveforms.	I, D	6, 7	<b>Course Result</b>	SLO was assessed by written test questions using the assessment criteria as stated in the course outline. <b>11</b> (10 male & 1 female) out of <b>13</b> students ( <b>84.6%</b> ) completed the CSLO.
				<b>Target Met</b>	Yes
2. Describe UJT oscillator operation.	3. Interpret schematic diagrams and waveforms.	I, D	6, 7	<b>Course Result</b>	SLO was assessed by written test questions using the assessment criteria as stated in the course outline. <b>12</b>

					(11 male & 1 female) out of <b>13</b> students ( <b>92.3%</b> ) completed the CSLO.
				<b>Target Met</b>	Yes
3. Describe SCR trigger control operation.	3. Interpret schematic diagrams and waveforms.	I, D	6, 7	<b>Course Result</b>	SLO was assessed by written test questions using the assessment criteria as stated in the course outline. <b>12</b> (11 male & 1 female) out of <b>13</b> students ( <b>92.3%</b> ) completed the CSLO.
				<b>Target Met</b>	Yes
4. Describe SCR power control operation.	3. Interpret schematic diagrams and waveforms.	D	6, 7	<b>Course Result</b>	SLO was assessed by written test questions using the assessment criteria as stated in the course outline. <b>13</b> (12 male & 1 female) out of <b>13</b> students ( <b>100%</b> ) completed the CSLO.
				<b>Target Met</b>	Yes
5. Describe SCR circuit troubleshooting.	3. Interpret schematic diagrams and waveforms.	I, D	6, 7	<b>Course Result</b>	SLO was assessed by written test questions using the assessment criteria as stated in the course outline. <b>11</b> (10 male & 1 female) out of <b>13</b> students ( <b>84.6%</b> ) completed the CSLO.
				<b>Target Met</b>	Yes
6. Describe the relationship between Triacs, Diac and four-layered devices.	3. Interpret schematic diagrams and waveforms.	I, D	6, 7	<b>Course Result</b>	SLO was assessed by written test questions using the assessment criteria as stated in the course outline. <b>13</b> (12 male & 1 female) out of <b>13</b> students ( <b>100%</b> ) completed the CSLO.
				<b>Target Met</b>	Yes
7. Describe the construction, operation and	3. Interpret schematic	I, D	6, 7	<b>Course Result</b>	SLO was assessed by written test questions using the assessment criteria as stated in the course outline <b>11</b>

application of Programmable Unijunction Transistor (PUT)	diagrams and waveforms.				(10 male & 1 female) out of <b>13</b> students ( <b>84.6%</b> ) completed the CSLO.
				<b>Target Met</b>	Yes

**Special comments:** 13 out of 13 or 100% of the students got a grade of C and higher.

**Summary of Grades:**

**A+ = 0**  
**A = 1**  
**A- = 2**  
**B+ = 2**  
**B = 3**  
**B- = 1**  
**C+ = 3**  
**C = 1**  
**C- = 0**  
**F = 0**

**Recommendations:** Laboratory equipment (NIDA cards) for discrete devices II must be enough for at least 3 to 5 sets to be able for the students to perform their required experimentation. Additional quality analog multi-meter and oscilloscope must also be purchase so that more hands on experimentation can be done.

Signature:      DANILO S. IBARROLA  
                          Instructor

**Date:** DEC. 2016