

Review of Performance: VEE 110 Discrete Devices I Spring 2017, (12 students, 10 males 2 females) P1
Submitted by: Danilo S. Ibarrola

Institutional Student Learning Outcomes (ISLO):

- ILO1:** Effective oral communication.
- ILO2:** Effective written communication.
- ILO3:** Critical Thinking
- ILO4:** Problem Solving
- ILO5:** Inter-cultural knowledge and competence.
- ILO6:** Information literacy.
- ILO7:** Foundations and skills for life-long learning.
- ILO8:** Quantitative reasoning.

Program Learning Outcomes (PLO)

- PLO1:** Practice Safety and occupational health procedures in the workplace.
- PLO2:** Use electronic tools and test equipment competently.
- PLO3:** Interpret schematic diagrams and waveforms.
- PLO4:** Build electronics projects to a given specification.

SLO#	Program SLO#	I, D, M	ISLO	Reflection/Comment	
1. Describe semiconductor diode parameters, operation and testing.	2. Use electronic tools and test equipment competently. 3. Interpret schematic diagrams and waveforms.	I,	3	Course Result	SLO was assessed by written test questions and experiments using the assessment criteria as stated in the course outline. 12 (10 male & 2 female) out of 12 students (100%) completed the CSLO.
				Target Met	Yes
				Students need more time in hands-on and other practical procedure to reach mastery level performance.	

<p>2. Familiarize in bipolar transistor parameters, operation and testing.</p>	<p>2. Use electronic tools and test equipment competently. 3. Interpret schematic diagrams and waveforms.</p>	<p>I, D</p>	<p>3</p>	<p>Course Result</p>	<p>SLO was assessed using hands-on experiment and written test questions using the assessment criteria as stated in the course outline. Students were able to familiarize themselves in bipolar transistor parameters, operation and testing. 10 (9 male & 1 female) out of 12 students (83.3%) completed the CSLO.</p>
				<p>Target Met</p>	<p>Yes</p>
<p>Students need more time in hands-on and other practical procedure to reach mastery level performance.</p>					
<p>3. Recognize Field effect Transistor (FET) parameters, operation and testing.</p>	<p>2. Use electronic tools and test equipment competently. 3. Interpret schematic diagrams and waveforms.</p>	<p>I, D</p>	<p>3</p>	<p>Course Result</p>	<p>SLO was assessed using hands-on experiment and written test questions using the assessment criteria as stated in the course outline. Students were able to recognize Field effect Transistor (FET) parameters, operation and testing. 11 (10 male & 1 female) out of 12 students (91.7%) completed the CSLO.</p>
				<p>Target Met</p>	<p>Yes</p>
<p>Students need more time in hands-on and other practical procedure to reach mastery level performance.</p>					
<p>4. Describe Metal Oxide Field Effect Transistor (MOSFET) parameters, operations and testing.</p>	<p>2. Use electronic tools and test equipment competently. 3. Interpret schematic diagrams and waveforms.</p>	<p>I, D</p>	<p>3</p>	<p>Course Result</p>	<p>SLO was assessed using hands-on experiment and written test questions using the assessment criteria as stated in the course outline. Students were able to describe Metal Oxide Field Effect Transistor (MOSFET) parameters, operations and testing. 11 (10 male & 1 female) out of 12 students (91.7%) completed the CSLO.</p>
				<p>Target Met</p>	<p>Yes</p>

				Students need more time in hands-on and other practical procedure to reach mastery level performance.	
5. Observe DC power supply operation.	2. Use electronic tools and test equipment competently. 3. Interpret schematic diagrams and waveforms.	I, D,	3	Course Result	SLO was assessed using hands-on experiment and written test questions using the assessment criteria as stated in the course outline. Students were able to observe DC power supply operation. 11 (10 male & 1 female) out of 12 students (91.7%) completed the CSLO.
				Target Met	Yes
				Students need more time in hands-on and other practical procedure to reach mastery level performance.	
6. Observe Integrated Circuit (IC) regulator and voltage doublers operation.	2. Use electronic tools and test equipment competently. 3. Interpret schematic diagrams and waveforms.	I, D	4, 6,7	Course Result	SLO was assessed using hands-on experiment and written test questions using the assessment criteria as stated in the course outline. Students were able to observe Integrated Circuit (IC) regulator and voltage doublers operation. 11 (10 male & 1 female) out of 12 students (91.7%) completed the CSLO.
				Target Met	Yes
				Students need more time in hands-on and other practical procedure to reach mastery level performance.	

Special comments: 11 out of 12 (10 males 1 females) or 91.7 % of the students got a grade of C and higher and 1 got F or 8.3 %.

Summary of Grades:

A+	=	0
A	=	0
A-	=	1
B+	=	2
B	=	4
B-	=	1
C+	=	2
C	=	0
C-	=	1
D+	=	0
D	=	0
D-	=	0
F	=	1

Recommendations Additional laboratory equipment such as analog and digital multi-meter and oscilloscope must be provided so that lab exercises will be well performed by the students per lesson. It is suggested that at least a maximum of 15 students per class with a 1:3 lab equipment ratios..

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