Review of Performance: (**VEE 240 Signal Processing**, Fall 2017, 10 students)
Submitted by: Nelchor Permitez Ed. D.

Institutional Student Learning Outcomes (ISLO):
ILO1: Effective oral communication.
ILO2: Effective written communication.
ILO3: Critical Thinking
ILO4: Problem Solving
ILO5: Intercultural knowledge and competence.
ILO6:Information literacy.
IL07:Foundations and skills for life-long learning.
ILO8:Quantitative reasoning.

Program Learning Outcomes (PLO)
PLO1: Practice Safety and occupational health procedures in the workplace.
PLO2: Use electronic tools and test equipment competently.
PLO3: Interpret schematic diagrams and waveforms.
PLO4: Build electronic projects to a given specification.
PLO5: Practice a career in the Telecomm Industry.
PLO6: Troubleshoot microwave, fiber optics and telephone system.

CSLO#	Program CSLO#	I, D, M	ISLO	Reflection/Comment
1. Describe analog pulse modulation circuit operation.	Interpret schematic diagrams and waveforms.	D	7	10 students (1 female and 9 male) out of 10 students (100%) successfully completed this CSLO as measured by using an hands on experiments and a written quiz.
2. Describe Pulse coded modulation (PCM) circuit, operation	Interpret schematic diagrams and waveforms.	D	7	10 students (1 female and 9 male) out of 10 students (100%) successfully completed this CSLO as measured by using an hands on experiments and a written quiz.

and troubleshooting PCM circuit.				
3. Describe Delta modulation (DM) circuit, operation ar troubleshoot DM circuit.	Interpret schematic diagrams and waveforms.	M	7	10 students (1 female and 9 male) out of 10 students (100%) successfully completed this CSLO as measured by using an hands on experiments and a written quiz.
4: Describe FSK (Frequency shift keying) circuit, operation and troubleshoot FSK circuit	Interpret schematic diagrams and waveforms.	M	7	10 students (1 female and 9 male) out of 10 students (100%) successfully completed this CSLO as measured by using an hands on experiments and a written quiz.
5. Describe Phase shift Keying (PSK) circuit, operation and troubleshoot PSK circuit.	Interpret schematic diagrams and waveforms.	M	7	10 students (1 female and 9 male) out of 10 students (100%) successfully completed this CSLO as measured by using an hands on experiments and a written quiz.
6. Describe and analyze Time and Frequency division multiplexing circuit operation and troubleshooting.	Interpret schematic diagrams and waveforms.	M	7	10 students (1 female and 9 male) out of 10 students (100%) successfully completed this CSLO as measured by using an hands on experiments and a written quiz.

Additional observations: Need to purchase additional set of NIDA cards to accommodate growing number of students enrolled in the course.

Special comments: 11 students were able to get a grade of "C" and higher.

Recommendations: Need to buy additional NIDA cards and console for Signal Processing.

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Nelchor Permitez
Professor Telecomm/Electronics
Pohnpei Campus